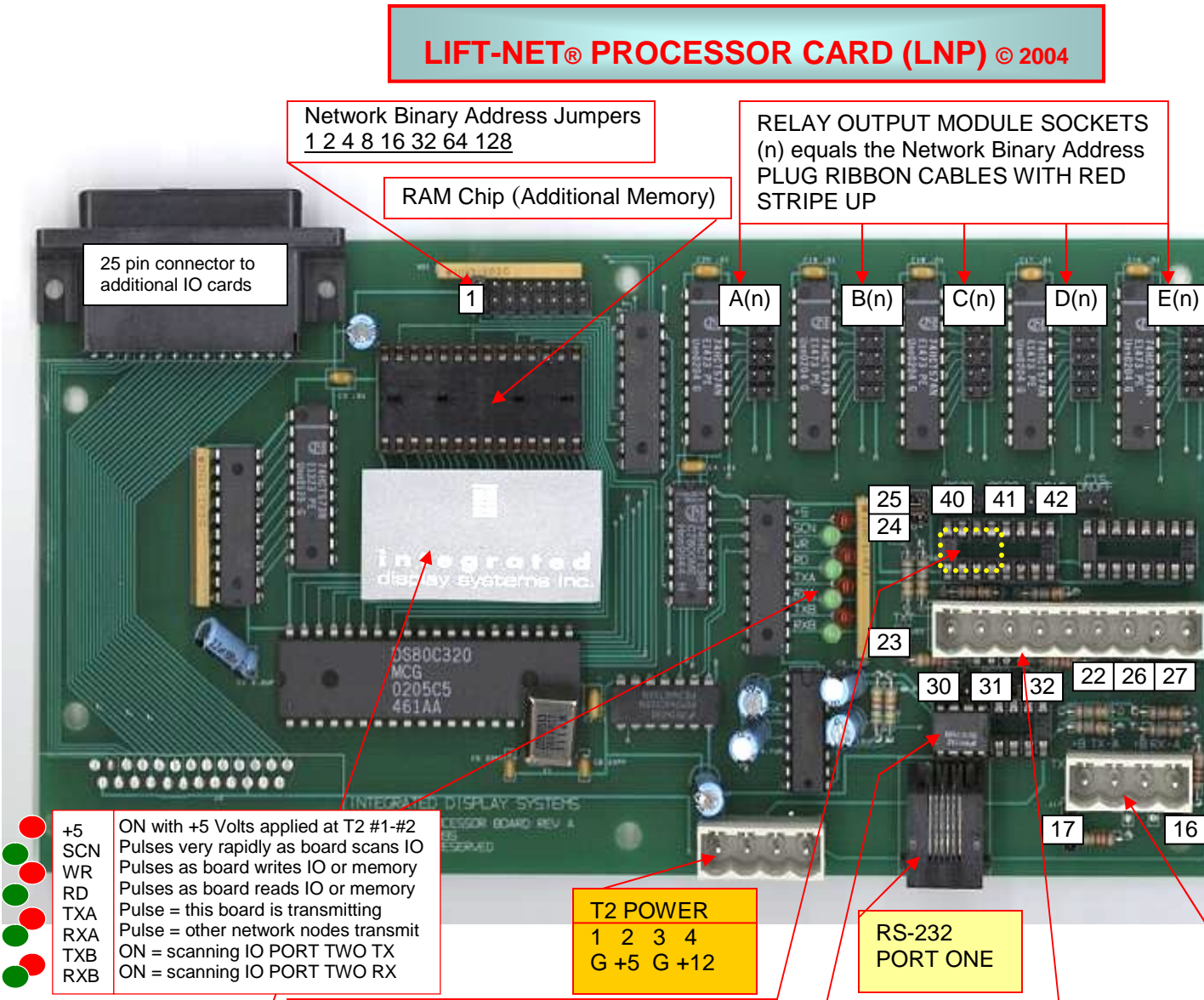


LIFT-NET® PROCESSOR CARD (LNP) © 2004



PORT ONE	PORT TWO

PIN JMP	END OF LINE	NOT END OF LINE	END OF LINE	NOT END OF LINE
J16	485 OFF	422 ON	485 OFF	422 ON
J17	ON	OFF	-	-
J22	-	-	ON	OFF
J23	-	-	ON	OFF
J24	-	-	ON	OFF
J25	-	-	ON	OFF
J26	ON	OFF	-	-
J27	ON	OFF	-	-
J30	ON	ON	OFF	OFF
J31	ON	ON	OFF	OFF
J32	ON	ON	OFF	OFF
J40	OFF	OFF	ON	ON
J41	OFF	OFF	ON	ON
J42	OFF	OFF	ON	ON
J1-J128	NETWORK BINARY ADDRESS JUMPERS			

- +5 ON with +5 Volts applied at T2 #1-#2
- SCN Pulses very rapidly as board scans IO
- WR Pulses as board writes IO or memory
- RD Pulses as board reads IO or memory
- TXA Pulse = this board is transmitting
- RXA Pulse = other network nodes transmit
- TXB ON = scanning IO PORT TWO TX
- RXB ON = scanning IO PORT TWO RX

T2 POWER
1 2 3 4
G +5 G +12

RS-232 PORT ONE

LNP EPROM CHIP
NEW 13 (-2001)
LNP-E (2001-2004)
LNP 0403 (2004-)
SWE (Escalator)
CUSTOM (pr job spec)

TRANSCEIVER CHIP PORT TWO RS-485
TO MAKE PORT TWO RS-422 ADD ADDITIONAL CHIP TO RIGHT, SHIFT JUMPERS 40-41-42 ONE PIN RIGHT

TRANSCEIVER CHIP PORT ONE RS-485
TO MAKE PORT ONE RS-422 ADD ADDITIONAL CHIP TO RIGHT, SHIFT JUMPERS 30-31-32 ONE PIN RIGHT

T1 RS-485 PORT TWO
1 2 3 4 5 6 7 8 9
B+ A- n/u n/u n/u n/u n/u n/u

T1 RS-422 PORT TWO
1 2 3 4 5 6 7 8 9
TX+ TX- RX+ RX- n/u n/u n/u

T3 RS-485 PORT ONE
1 2 3 4
B+ A- n/u n/u

T3 RS-422 PORT ONE
1 2 3 4
TX+ TX- RX+ RX-

